Intusoft Newsletter

Personal Computer Circuit & System Design Tools



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In This Issue

12 EKV Model

Power Supply Design

User Interface Tips

Perfecting Spicemod

13 New Scope5 Scripts

and ICL Commands

Power Supply Design

ntusoft's software tools are widely used to synthesize and analyze power supplies. Over time, an increased feature set has made these tools more difficult to use because of the many ways the features can be combined to produce an acceptable design. To simplify the software use, we have introduced a comprehensive end-end design method for switch mode DC-DC converters. The framework consists of:

1

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9

•	New SMPS design wizards for	
	Transformer/Inductor Design	

- Template drawings for Flyback and Forward/Buck Regulators
- Large signal "Average" models
- "Average" models with transport delay
- New "Average" models that predict efficiency using DC analysis
- A special SMPS Spice
 convergence option VSECTOL
- New Optimization techniques for compensation network design
- New EMI compliance test circuits
- New One button Scope5 scripts for EMI evaluation
- One button Scope5 scripts for Bode Plots

Starting with the Magnetics Designer, MD, using the Switch Mode Power Supply wizards; the user enters the power supply specification and MD computes the Transformer or Inductor spreadsheet entries. Data needed are input power supply voltage and the output voltage(s)current(s) along with frequency, duty ratio and ripple current.

An automatic design phase synthesizes the transformer/ inductor. The user can then interact with MD to perfect the design. When acceptable results are achieved, an IsSpice model is generated for use in one of the template power supplies. The demo version produces designs with limited number of winding and core selection that are compatible with the ICAP/4 demo.

A number of working power supply designs have been placed in a template framework. These templates include Flyback, Forward and Push-Pull topologies. The demo software will run average and switching models for these pre-designed templates. These templates accept the IsSpice transformer models generated using Magnetics Designer for cycle by cycle switching simulations. The transformer parameters can be input to the "Average" models.

New "Average" models for Forward and Flyback work for both large and small signals with auto CCM/DCM switching and account for transport delay using a first order hold and Z-transform integrator.

There are 2 classes of models, in the demo they are FORWARD_ID, and FLYBACK_ID and in the professional additional capabilities are included in the FORWARD_I and FLYBACK_I. These professional versions have resistive switching losses and dynamic switching losses modeled. They give a reasonable estimate of power supply efficiency just from the OP analysis.

Included are objective functions for optimization of

transient response and selection of input filter damping. Again, the optimizer can be run using the demo software. (Newsletter 63, pp7-10)

Intusoft's new VSECTOL option (Newsletter 60, pp7-8) has been set in the transient simulations to yield improved accuracy using fewer iterations than can be obtained with traditional Spice time step control methods.

A built-in optimizer aids the designer in selecting critical control system parameters. It gets the most out of a design topology; frequently saving extra parts by getting the right transient wave shapes using the optimized component values. See http://www.intusoft.com/articles/PwrTemplate.pdf> for an example and instructions on using the optimizer for power supply design.

Both MIL-STD-462 and the EU CISPR16 line stabilization networks are used in the template power supplies to provide a test signal that's used by the Scope5 waveform viewer to plot the EMI spectrum shown in figure 1 along with the



Figure 1. EMI plot shows measurement and CISPR16 specification

Page 3









Figure 3. The Flyback SMPS wizard

Page 4

In the past the MD data entry was difficult to compute because it's up to the user to specify inductance, DC current, AC current, maximum current and Volt-second capacity. These parameters decouple the inductor design from application specific waveform shapes, but it requires quite a bit of pencil and paper or finger and keyboard calculation.

We initialize the core family to one suitable for SMPS design. There are many more, including pot cores, planar cores and many other Ferrite geometries, different vendors and materials. What you need to input is your choice of frequency, duty ratio and percent ripple.

The percent ripple is the pk-pk ripple with respect to the peak current. At 100% the power supply slips into discontinuous mode and you can select a smaller secondary duty ratio. DCM operation is pretty stressful so that you should use it only for low power designs.

Making different designs with varying ripple and duty ratio lets you trade off DC stress (small AC ripple) vs. AC stress (high AC ripple). There is probably an optimum value over the range of 0 to 100% Iripple; however, it will depend on your tolerance for things like Litz wire, multiple strands and foil wire.

Our power supply will run with rectified DC from USA or EU power. Typically the DC voltage will be 150 or 300. Now the question is, what to use for the design point? In our experience, the best design comes from the lower voltage (higher current) specification, so we'll enter 150 for the winding 1 voltage and press the Insert button to get a new winding for data entry. The voltage should include the rectifier drop so we'll input 5.5 for the DC voltage and 10 for the current.

The efficiency includes transformer and switching losses. That's it, unless you want more outputs. Press the Apply button and MD will run through an automated design picking the smallest core which keeps the estimated temperature rise below the maximum rise (50 Deg C is the default). Here's the result after tweaking the design using techniques discussed in the MD documentation in http://www.intusoft.com/lit/MagDesRev01.pdf. The time to make this design was under 3 minutes.

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Figure 4. Bobbin tab shows the final design.

Predicted loss is 1.1 watts at 300 Volt input and 1.3 watts for the 150 Volts input. The leakage inductance is 2.7 micro henries, all done using Heavy Formvar wire. The resulting IsSpice model can be either copied and pasted into the template drawing or saved in the parts library.

Designs using Litz wire can withstand more ripple and operate at higher frequencies producing smaller magnetic components; however, Litz wire is more difficult to use and is more expensive. That's an economic and perhaps an electro-political decision you must make.

The gap field causes eddy current losses in the wire near the gap. Moving the wire at least 3 gap lengths away from the gap, using Litz wire, moving the gap or breaking the gap up into many small pieces can reduce this loss. In the limit, a distributed gap ferrite; for example kool mu, will not have any gap loss. These properties are all predicted using Magnetics Designer.

The inductance, turns ratio and leakage inductance are all parameters used in the average models. These values are in the IsSpice model. The user needs to re-scale the template part values based on the frequency and power levels. Once that is done, the control system can be designed, first making it stable using the average model AC analysis and Bode Plots, then refine the values using the average models transient analysis and optimizer.

Finally the transient switching model can be run to validate the average model results and measure dynamic power transistor stress as shown in Figure 5.



Figure 5. Scope5 uses parametric cursors for load-lines

Then, the snubbers component values can be optimized and EMI tests can be run to iterate the design further to gain compliance.

The Magnetics Designer SMPS wizards are configured for 4 transformer types;

- 1 Flyback
- 2 Forward
- 3 Bridge
- 4 push-pull

Each of these has one or more corresponding template drawings. Each template drawing has a transient switching configuration and an average model. Some of the template drawings also have hierarchical sub drawings for the average and switching models that facilitate model development.

User Interface Tips

Here are some features that have been added over time that some of you may have overlooked.

- Using diagonal wires in SpiceNet: Press 'w' to get the wire tool; then press the left mouse button while pressing the <Ctrl> key. You will be able to connect 2 points in a diagonal direction. No intermediate connections are possible.
- 2. Customizing toolbars: Both IntuScope and SpiceNet allow you to construct custom toolbars. Use the "options\customize toolbars" menu and drag icons into the toolbar. Dragging them off the toolbar removes them.
- 3. Making custom calculator entries: Look at the !User folder in <IcapsDir>\in\scripts. All you have to do is make a text file with a ".scp" extension in this folder or any other folder within the "Scripts" folder and the

script file will be immediately available for you in the Scope5 Calculator menu. By adding a 16 wide by 15 high bitmap with the same base name, you can insert it in the toolbar to make a one button script.

Perfecting Spicemod

During the model testing in Spicemod5, we noticed that the simulated TR and TS values did not match the values entered into Spicemod5 for certain Darlington transistors. For example, if a ZTX614 NPN macro model created in Spicemod5, described in Figure 6, was run in the DarlNPN.DWG test schematic, we would get the measurements of TS=3.030u and TR=281.3n. Compared with the Spicemod5 default values, TS = 2.8u and TR=615n; the SPICE simulated measurements were about 8.21% and 54.2% off from the desired TS and TR respectively. Our software modeling guru told us that it's nearly impossible to make an exact solution and that some kind of iteration using IsSpice was almost certainly required.



Figure 6. ZTX614 NPN Model created in Spicemod5

Page 9Page

The newly added ICAP/4 Window optimizer will help us to obtain the exact TR and TS values desired in the simulator.

Since TS and TR values measured are determined by the TF and TR parameters specified in the QPWR NPN model, we have modified the sm_DarlNPN netlist, as shown in Figure 7, to pass the TF and TR parameters to the schematic level, because only schematic level parameters can be optimized.



Figure 7. sm_DarlNPN netlist modification

These changes set the model parameters, TF and TR, to TFF and TRR. Two new parameters, TFM and TRM, were created in the params.txt file for initializing the TFF and TRR values during the optimization. TFM*2 and TRM were chosen to be the initial values for TFF and TRR, and 50% optimizing percentage was used for both TFF and TRR under the Tolerance/Sweep/Optimize tab in the Subcircuit Properties dialog, illustrated in Figure 8. The initial values and optimizing percentage are adjustable, depending on the optimization goal. Furthermore, an objective function was defined by using the script shown in Figure 9.

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Figure 8. Initial values and optimizing percentage

```
moveCursorRight(0,v(3),6)
Tst = getCursorX(0)
setcursor(0,50u)
setcursor(1,100u)
range = getcursory(v(3),1) - getcursory(v(3),0)
left = getcursory(v(3),0) + .1 * range
right = getcursory(v(3),1) - .1 * range
moveCursorRight(0, v(3), left)
moveCursorLeft(1, v(3), right)
Tri = getcursorx(1) - getcursorx(0)
ofunc ={(TS/TR)^2}*(Tri-{TR})^2 + (Tst-{TS})^2
ofunc = ofunc * 1e15
print ofunc
Figure 9. Script for the Optimizer Objective Function
```

The optimize script was modified to do three passes using a 3rd order polynomial with 7 data points. By running the optimizer3rd three times, objective function and the optimized TRM and TFM values were obtained. The objective function should be close to 0. Replacing the TRM and TFM*2 with the bestval (optimized TRM and TFM values) in the Subcircuit Properties dialog would achieve the goal of having the TR and TS values to match the desired values.

EKV Low Power MOSFET Model

MOS IC technology has made tremendous progress in recent years. The ever-increasing trend toward low-voltage and lowpower deep submicron CMOS design has created a need for a more physical and compact MOS model. A clear understanding of the MOS transistor operation at very low current and a correct modeling in the weak and moderate inversion regions, which have been ignored by many designers and model builders for years, have become even more important issues for cost-effective design of high-performance analog and digital integrated circuits.

Invented at EPFL in Lausanne Switzerland, the EKV MOSFET model is a scalable and compact simulation model built on fundamental physical properties of the MOS structure.



This model is dedicated to the design and simulation of lowvoltage, low-current analog, and mixed analog-digital circuits using submicron CMOS technologies. Its physical basis ensures excellent modeling of weak-to- strong inversion behavior for current, transconductances and gm/Id ratio. Due to the model's in-



creased availability in circuit simulators the EKV model is a powerful design tool, giving the designer insight into device and circuit operation and allowing faster

The EKV MOSFET model is in principle formulated as a 'single expression', which preserves continuity of first-and higher-order derivatives with respect to any terminal voltage, in the entire range of validity of the model. The EKV MOSFET model version 2.6 has been recently implemented in IsSpice as MOS model level 9.

The following physical effects are included in this model:

- Basic geometrical and process related aspects as oxide thickness, junction depth, effective channel length and width
- Effects of doping profile, substrate effect
- Modeling of weak, moderate, and strong inversion behavior
- Modeling of mobility effects due to vertical and lateral fields, velocity saturation
- Short-channel effects as channel-length modulation (CLM), source and drain charge-sharing (including for narrow channel widths), reverse short channel effect (RSCE)
- Modeling of substrate current due to impact ionization
- Quasi-static charge-based dynamic model
- Thermal and flicker noise modeling
- First-order non-quasi-static model for the transadmittances
- Short-distance geometry- and bias-dependent device matching.

For a detailed description of the EKV MOSFET model please visit http://legwww.epfl.ch/ekv/model.html

New Scope5 Scripts and ICL Commands

Here are a few more new scripts you'll find in Scope5. You can make your own scripts by following the format below and placing them in a ".scp" file in the ...in\scripts subfolders. The scripts subfolders are the same as the hierarchy shown in the calculator menu. The first line is a comment line that will be displayed in the help status at the bottom of the main window. The character following the optional '&' symbol is the hotkey. If you create a bitmap (15 hi by 16 wide), naming it with the same name as your script but with a .bmp extension, you can make it a toolbar button using the options menu customize

Page 13

toolbar dialog. You can also add a .htm file to provide further information about your script.

!User\expandx:

This one illustrates the simplicity and power of the script language. You place the cursors somewhere on the waveform and press x. The plot's x-axis is zoomed in to the region between the cursors.

http://www.intusoft.com/script/pages/commands3.htm#setxlimits

```
*&x expands xscale to the cursors
setxlimits getcursorx(0) getcursorx(1)
```

!User\expandy:

Similary to expand x except it makes the y-axis scaling for the selected waveform full scale.

http://www.intusoft.com/script/pages/commands3.htm#setylimits

```
*&Y expands yscale to maximum for current
waveform
setylimits min(current) max(current)
```

Functions\matchscales:

Makes the y-axis scales of all waveforms the same as the current (selected) waveform. Notice how the hotkey combination works (x Y y) to expand and link scales in both x and y axis.

```
*&y Match all y-axis scales to that of the
current trace.
constants.max = maxscale(current)
constants.min = minscale(current)
alias refvec current
nv = nextvector(null)
while nv <> null
    if refvec <> nv
        setvec nv
        setvec nv
        setylimits min max
    end
    nv = nextvector(nv)
end
setvec refvec
```

!User\frequency:

Works like a frequency counter to measure the period of the 10 cycles after the position of cursor 0. The w hotkey was used because f is already assigned to the fft.

```
*&w frequency over next 10 intervals from
cursor 0
triqval = mean(current)
trigchange = pk_pk(current)/10
setnthtrigger 0 current
postrig trigval
trigchange 1
setcursor 1
getcursorx(0)
setnthtrigger 1 current
postrig trigval
trigchange 10
frequency = 10 /
(getcursorx(1) -
getcursorx(0) )
setunits frequency Hz
print frequency
```

The latest release also includes the askvalues command, that prompts for user entry, see http://www.intusoft.com/script/pages/commands.htm#askvalues, for details.

Coming Soon

Smith charts and TouchStone file reader along with scripts for easy management of these data.

A new "append" command will be added to the ICL. It concatenates vectors and it will be used in a "superscript" function in Scope5 that collects all vectors in a plot into a single family. That's useful in managing alter dialog sweeps and plot-all results.

And finally; SpiceNet gets rubber banding. This was delayed because of the added complexity caused by having to consider what to do with parts on other layers not belonging to the configuration being edited. We decided to move them also if there were any layers common to both configurations.

Page 15

Intusoft's World-Wide Support Staff

Italy

Listed below are dealers where Intusoft products, updates, information, and support may be obtained.

Dahan Tech Inc. - Sang Y. Cho South Korea Tele: 82-2-515-2845/FAX: 82-2-515-2844 email - sycho@dahan.co.kr Web: http://www.dahan.co.kr

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Tele: 44-01223-516469/FAX: 44-01223-729-916 email - info@softsim.com Web: http://www.softsim.com

DS-Design Systems OY-Hannu Tikkanen Finland

Tele: 358-14-652588/FAX: 358 -14-610725 email - sales@designsystems.fi Web: http://www.designsystems.fi

Thomatronik GmbH - Herbert M. Müeller

Germany, Austria, Croatia, Slovenia, Czech Rep. Tele:49 -8031-2175-0/FAX: 49- 8031-2175-30 email - info@thomatronik.de Web: http://www.thomatronik.de

Cho Chieh Enterprise Ltd. - Tennyson Lin

Taiwan, Hong Kong, China, Southeast Asia Tele: 886-2-2981-2187 FAX: 886-2-8983-5229 email - webmaster@chochieh.com.tw Web: www.chochieh.com.tw

COREDA Corporation - Monique Masserey Western Canada

Tele: (905) 566 - 1755 Tele: (877) 566 - 1755 Toll Free FAX: (905) 566 9925 e-mail: moniquem@coredacorporation.com

EDAforce , Inc - Jean Godbout Eastern Canada Tele: (450) 622-5500 FAX: (450) 629-4211 e-mail: jean@edaforce.com Web: www.edaforce.com

Elbo - Bogdan Olejarczyk Poland \$\$\$\$#7\$\$.r.(43\72)\$#193\$5\$\$\$#14a email - elbo_@astercity.net Tele: 39-02-48022546/FAX: 39-02-48015146 email - vscibilia@siscad.it

CMR Design Automation - Mahesh Chandra India

Tele: 91 11 6477085/Fax:91 11 6213498 email - cmreda@bol.net.in

ChipCAD - Tibor Berky Hungary

Tele: 36-1 399-4290/FAX: 36-1 399-4299 email - tholman@chipcad.hu Web: http://www.chipcad.hu

IVIS Co., Ltd. - Hiro Nagano

Japan Tele: 81-45-332-5381/FAX: 81-45-332-5391 email - sales@i-vis.co.jp Web: http://www.i-vis-co.jp

EDA Software - John Meltezos

Greece Tele: 301-8256258-9/FAX: 301-884-1016 email - sales@edasoft.gr

INTSYS Europe SA - Claude Masseboeuf

France, Tunisia, Algeria, Morocco Tele:(33)01-60-81-00-69/FAX:(33)01-60-81-00-70 email - sales@intsys-europe.fr Web: http://www.intsys-europe.fr

DFM - Tuvia Liran Israel Tele: 972-4-9533059/FAX: 972-4-9533057 email - tuvia@dfm4vlsi.com Web: http://dfm4vlsi.com

TECH 5 - Geert Mosterdijk Netherlands, Belgium, Luxenburg Tele: 31-184-6155-51/FAX: 31-184-6154-51 Tele:32-2-657-31-64/Fax:32-2-657-49-25 Belgium email: info@tech5.nl Web: www.tech5.nl

Page 16